

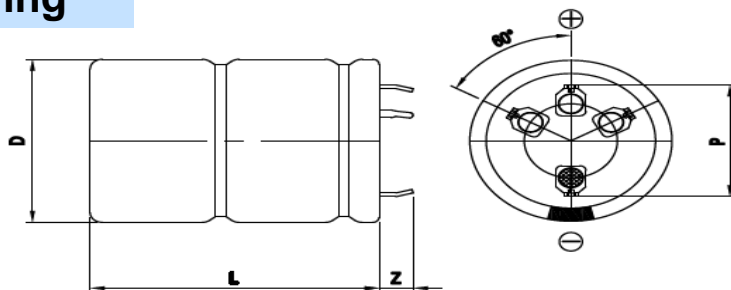
## Features

### EDLC (Electric Double Layer Capacitor)

- High Power Density (Low ESR)
- Over 500,000 cycle life (semi-permanent)
- Short-term Peak Power assist applications



## Drawing



D (Φ)	35.0+1.5 Max
L (mm)	82.0±2.0
Z (mm)	6.0 ±1.0
P (mm)	23.0 ±0.2

## Specification

Item	Characteristics	
Product series	EDLC	
Rated Voltage (V <sub>R</sub> )	2.7V	
Operating Temperature	-40 ~ +65°C	
Capacitance Tolerance	-10 ~ +30%	
High Temperature Load Life	After 1,000 hours at V <sub>R</sub> loaded under +65°C, capacitors meet the following criteria.	
	Capacitance Change	≤ 30% of initial value
	ESR	≤ 2 times of specified value
	85°C Temperature	Max. 2.3V
Cycle Life Characteristics	Cycle	Over 500,000
	ΔC	≤ 30% of initial value
	ESR	≤ 2 times of specified value
	Method	Cycle of Charge/discharge from V <sub>R</sub> to 1/2V <sub>R</sub>
Shelf Life	2 Years No Electrical Charge, Temperature below 70°C (ΔC : ≤ 10% of initial value / ΔESR : ≤ 50% of initial value)	

Part Number	Rated Voltage (V)	Capacitance (F)	ESR (mΩ)		Max. Current (A)	Leakage Current (mA, 72hr)	Size (mm)
			AC(1kHz)	DC			D × L
VEC 2R7 507 QG	2.7	500	3.0	4.5	205	1.00	35×82

\* Max. Current : 1 sec. discharge to 1/2V<sub>R</sub>

Version 9.2 2019.02.21.